

MODULE BUS

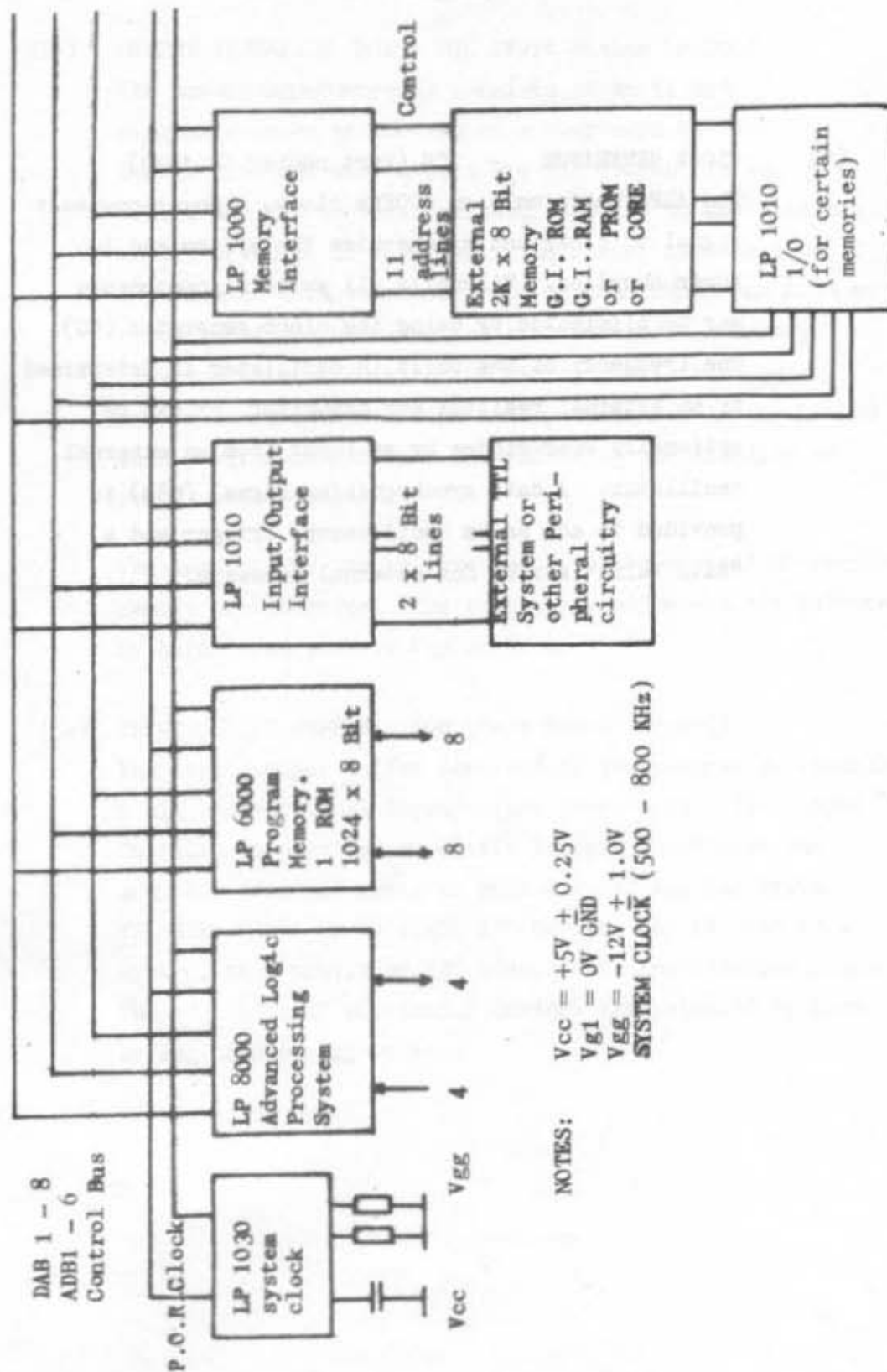


FIG. 1 BLOCK DIAGRAM OF ADVANCED LOGIC PROCESSING SYSTEM A.L.P.S.